Meeting of the

President's Council of Advisors on Science and Technology (PCAST)

May 12, 2022

Invited Speaker Biographies

(*in order of presentation*)

PATRICK (PAT) GELSINGER

Patrick (Pat) Gelsinger is chief executive officer of Intel Corporation and serves on its board of directors. On Feb. 15, 2021, Gelsinger returned to Intel, the company where he had spent the first 30 years of his career. Before rejoining Intel, Gelsinger was CEO of VMware. In that role, he transformed VMware into a recognized global leader in cloud infrastructure, enterprise mobility and cyber security – almost tripling the company's annual revenues. Gelsinger was also ranked the best CEO in America in 2019, according to an annual survey by Glassdoor. Prior to joining VMware in 2012, Gelsinger was president and chief operating officer of EMC's Information Infrastructure Products business, overseeing engineering and operations for information storage, data computing, backup and recovery, RSA security and enterprise solutions. Gelsinger began his career in 1979 at Intel, becoming its first chief technology officer, and also serving as senior vice president and the general manager of the Digital Enterprise Group. He managed the creation of key industry technologies such as USB and Wi-Fi. He was the architect of the original 80486 processor, led 14 microprocessor programs and played key roles in the Intel® Core™ and Intel® Xeon® processor families, leading to Intel becoming the preeminent microprocessor supplier. Gelsinger earned several degrees in electrical engineering: an associate degree from Lincoln Technical Institute, a bachelor's degree from Santa Clara University and a master's degree from Stanford University. He holds eight patents in the areas of VLSI design, computer architecture and communications, is an IEEE Fellow, and serves as a member of the National Security Telecommunications Advisory Committee. Gelsinger and his wife have been married for over 30 years; they have four children and eight grandchildren. He is also a published author and speaks frequently on faith, work and philanthropy.

PRIYANKA RAINA

Priyanka Raina is an Assistant Professor of Electrical Engineering at Stanford University. She received her B.Tech. degree in Electrical Engineering from Indian Institute of Technology Delhi in 2011 and her S.M.

and Ph.D. degrees in Electrical Engineering and Computer Science from Massachusetts Institute of Technology in 2013 and 2018. Raina's research is on creating high-performance and energy-efficient architectures for domain-specific hardware accelerators in existing and emerging technologies, and agile hardware-software co-design. Her research has won best paper awards at ESSCIRC and MICRO conferences and in the JSSC journal. She is also the recipient of Intel Rising Star Faculty Award, Hellman Faculty Scholar Award and is a Terman Faculty Fellow.

RODRIGO LIANG

Rodrigo Liang is the CEO and co-founder of SambaNova Systems. He is a business and engineering leader who recognized the fundamental shift that AI will have on the world. He and his co-founders designed a new full-stack hardware and software platform optimized for AI workflows and set the company on a mission to enable the future of AI today. Prior to founding SambaNova, Liang was the senior vice president responsible for SPARC Processor and ASIC development at Oracle. During his tenure at Oracle, he led one of the industry's largest engineering organizations in developing high-performance microprocessors and releasing 12 major SPARC processors and ASICs for enterprise servers. SPARC processor performance achieved numerous world records, and it continues to be a performance leader or enterprise applications. Before joining Oracle via the Sun acquisition in 2010, Liang was vice-president at Sun Microsystems where he worked on the development of the world's first multi-core processors, code-named Niagara. Liang holds master's and bachelor's degrees in electrical engineering from Stanford University.

AART DE GEUS

Since co-founding Synopsys in 1986, Aart de Geus has expanded the company from a technology start-up to the world leader in Electronic Design Automation (EDA), Silicon IP, and application security testing, offering complete solutions from Silicon to software design. For the last 35 years, de Geus has enthusiastically led Synopsys to deliver the automation breakthroughs required to advance electronic design at the rate of Moore's law and beyond. Today, virtually all advanced chip designs rely on Synopsys' software and building blocks. Considered one of the world's EDA pioneers and most influential semiconductor industry executives, de Geus has been recognized for his technical, business and community achievements with numerous awards, including: IEEE Robert N. Noyce Medal and IEEE Fellow, Global Semiconductor Association "Morris Chang Exemplary Leadership" Award, Electronic Business Magazine's "CEO of the Year," Silicon Valley Leadership Group "Lifetime Achievement" Award, Phil Kaufman Award for distinguished contributions to EDA, Silicon Valley Engineering Council Hall of Fame Award, Joint Venture Silicon Valley "David Packard" Award for Civic and Community Service, and Member of the United States National Academy of Engineering. de Geus serves on the Boards of Applied Materials, Inc., the Silicon Valley Leadership Group (SVLG), the Global Semiconductor Alliance (GSA), and the Electronic System Design (ESD) Alliance. He actively promotes next-generation STEM education through project-based science and math learning sponsored by the Synopsys Outreach Foundation, which he created in 1999.

JOHN NEUFFER

John Neuffer is President and CEO of the Semiconductor Industry Association (SIA), which has been the voice of the chip industry for over four decades. He has been at the nexus of technology, public policy,

and trade for most of his career. Since joining SIA in 2015, Neuffer has led the association's policy advocacy in Washington and capitals around the world to foster growth and innovation in semiconductor design, manufacturing, and research. He also serves as a member of the Board of Directors of the Semiconductor Research Corporation, the world's leading non-profit industry-government-academia microelectronics research consortium. Prior to joining SIA, Neuffer served as Senior Vice President for Global Policy at the Information Technology Industry Council, where he led a global team to expand market access opportunities for member companies around the world. He directed all global government relations in trade, cybersecurity, standards, regulatory, Internet governance, and privacy. Previously, Neuffer served for over seven years at the Office of United States Trade Representative (USTR) in Washington, DC: two years as Deputy Assistant U.S. Trade Representative for Asia-Pacific Economic Cooperation (APEC) Affairs, preceded by over five years as Deputy Assistant U.S. Trade Representative for Japan. Prior to his tenure with USTR, Neuffer was a Senior Research Fellow and Political Analyst with the Mitsui Kaijyo Research Institute in Tokyo for nine years. As a leading commentator on Japanese politics and policy at the institute, he published a widely read newsletter and wrote regular commentary for the Asian Wall Street Journal, TheStreet.com and Newsweek Japan. All told, Neuffer lived in Japan 11 years. He is a native of Montana and Washington State.